PATENT ABSTRACTS OF JAPAN

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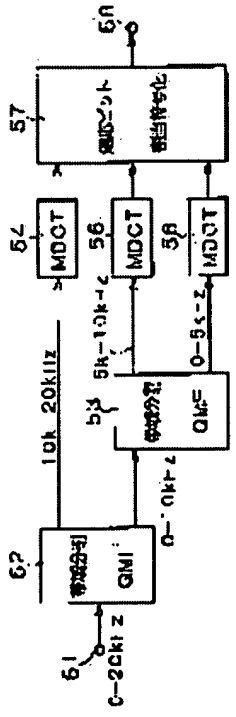
(54) CONVERSION CALCULATION DEVICE AND INVERSION CALCULATION DEVICE FOR IMPROVED DCT AND CONVERSION CALCULATION METHOD FOR IMPROVED DCT

(57)Abstract:

PURPOSE: To reduce a work area of an FFT calculation section and an arithmetic operation circuit by employing an FFT whose length is N/4 with respect to N sets of time series sample data segmented while being overlapped by 50% each with both adjacent blocks to process the data.

CONSTITUTION: An audio PCM signal whose frequency is 0-20kHz inputted to an input terminal 1 is divided into bands whose frequency is 0-100kHz and 10-20kHz by a band split filter 52 and divided into bands whose frequency is 0-5kHz and 5-10kHz by a band split filter 53. The 10-20kHz band signal from the filter 52 is inputted to an MDCT circuit 53. The signal of the 0-5kHz and 5-10kHz from the filter 53 is inputted respectively to MDCT circuits 55, 56, in which MDCT is processed. Spectrum data or coefficient data are integrated for each critical band and the result is inputted to an adaptive bit allocation coding circuit 57, from which coded data are outputted from an output terminal 58.



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